



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ronald D. McCallister

Serial No.: 10/766,779

Group Art Unit: Unknown

Filed: 27 January 2004

Examiner: Unknown

For: PREDISTORTION CIRCUIT AND METHOD FOR COMPENSATING NONLINEAR DISTORTION IN
A DIGITAL RF COMMUNICATIONS TRANSMITTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. § 1.56, the references listed on the attached form PTO/SB/08B are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the cited documents are enclosed.

This information is being submitted subsequent to the later of three months after the filing date of the present application but before the first mailing of the first Office Action on the merits.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached form PTO/SB/08B with initials or other appropriate marks.

Respectfully submitted,

Lowell W. Gresham

Attorney for Applicant

Registration No. 31,165

6 May 2004
MESCHKOW & GRESHAM, P.L.C.
5727 North Seventh Street
Suite 409
Phoenix, Arizona 85014
(602) 274-6996



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CERTIFICATE OF MAILING

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

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I hereby certify that this correspondence, consisting of this Certificate of Mailing; Information Disclosure Statement; PTO-1449 Form and all references cited; Preliminary Amendment; and a Postcard, are being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

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6 May 2004
Date

6 May 2004

MESCHKOW & GRESHAM, P.L.C.
5727 North Seventh Street
Suite 409
Phoenix, Arizona 85014
(602) 274-6996

Lowell W. Gresham
Signature

Respectfully submitted,

Lowell W. Gresham
Lowell W. Gresham
Attorney for Applicant
Registration No. 31,165

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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/766,779
Filing Date	27 January 2004
First Named Inventor	Ron McCallister
Art Unit	
Examiner Name	
Attorney Docket Number	2298-020

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	/Emmanuel Bayard/	Date Considered	10/01/2007
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* EXAMINER. Include if reference considered, whether or not citation is in conformance with MPEP § 608. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * Applicant's unique citation designation number (optional). * See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP § 901.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. * Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet **2** of **2**

Application Number	10/766,779
Filing Date	27 January 2004
First Named Inventor	Ron D. McCallister
Art Unit	
Examiner Name	
Attorney Docket Number	2298-020

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/E.B./		GLENTIS, KOUKOULAS, & KALOUPSIDIS "Efficient Algorithms for Volterra System Identification" IEEE Transactions on Signal Processing, Vol 47, No. 11, Nov 1999.	
		DING, RAICH, & ZHOU "A Hammerstein Predistortion Linearization Design Based on the Indirect Learning Architecture" Georgia Inst. of Tech. School of Elect. & Comp. Engineering.	
		RAZZAGHI & CHANG "A 10-b, 1GSample/s ADC using SiGe BiCMOS Technology" Electrical Engineering Department, UCLA.	
/E.B./		DANESHRAH "DSP VLSI Engine for Electronic Linearization of Fiber Optic Links" Wireless Integrated Systems Research Group, UCLA, 02/27/2001.	
/E.B./		FOX "Analysis and Dynamic Range Enhancement of the Analog-to-Digital Interface in Multimode Radio Receivers" Virginia Polytechnic Inst. & State Univ. 02/1997.	
/E.B./		HUMMELS "Linearization of ADCS and DACS for All-Digital Wide-Bandwidth Receivers" Depart. of Electrical & Comp. Engineering, Univ. of Maine, 09/09/1999.	

Examiner Signature	/Emmanuel Bayard/	Date Considered	10/01/2007
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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